**Shishira K Das**

**EDUCATION**

* **B.Tech** **VLSI Design**

**CGPA – 8.5 / 10 2019-2023**

Amrita Vishwa Vidyapeetham

* **B.Tech** **Electronics and Communication Engineering**

**CGPA – 7.0 / 10 2019-2023**

APJ Abdul Kalam Technological University, Kerala

* **Class 12** – 90% **2019**

Institution:

* **Class 10** – 95% **2017**

Institution:

**PROJECTS**

**VIP development for AMBA AHB DAC controller using UVM**   
**Methodology**   
Developed complete TB setup for AHB DAC Controller based on UVM Methodology

Implemented TB components including Environment, Agent, Driver, Monitor, Sequencer, Scoreboard, random constraints-based sequences.

Planned, documented and implemented the TestPlan as per the specification.

Assertion based checkers are implemented to verify various features.

**Implementation and Verification of FIFO using System Verilog concepts**

RTL implementation of FIFO based on the given specification.

Verified the RTL coded FIFO using System Verilog concepts like Interfaces and OOPs concepts.

Assertion based checkers are implemented to verify various features.

Coverage is coded to ensure all the features are covered in verification

**Implementation and Verification of Dual-Port SRAM using System Verilog concepts**

RTL implementation of Dual Port SRAM based on the given specification.

Verified the coded RTL using System Verilog concepts like Interfaces and OOPs.

**Design of PIR based Human detection and counting system**

Designed and implemented a circuit for human detection using PIR sensors using Embedded C.

**TECHNICAL SKILLS**

System Verilog, Verilog HDL, VHDL, Cadence NC-Verilog, NC-SIM, ModelSim, Pspice, LTSpice, Microsoft Office, VisEcad, Zuken board, AMBA AHB-Lite, AMBA APB, SPI, C++, Arduino, Embedded C, OOPs,

**INTERNSHIP**

**BOSCH GLOBAL SOFTWARE TECHNOLOGIES PVT.LTD (August 2022- Present)-Internship**

Project Trainee in Bosch Global Software Technologies Pvt. Ltd

Electronics Hardware Development in ADAS in automotive industry.

Experience in High speed Designs

Hardware development of memory modules (DDR, LPDDR, etc)

**Internship at Keltron REC-H249 on embedded system**

Basic code for ARDUINO UNO using C++.

Exposure on various Development Stages: Product Hardware Firmware

**LANGUAGES**

English, Malayalam, Tamil